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,	Application No.	Applicant(s)	
Nation of Allamability	10/629,512	CHEN ET AL.	
Notice of Allowability	Examiner	Art Unit	
•	James Cho	2819	7
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to the amendment filed 1-18-2005.			
2. The allowed claim(s) is/are <u>1-7,10-13 and 15-19</u> .			
3. The drawings filed on 22 December 2003 are accepted by the Examiner.			
 4.			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date	6. ☐ Interview Summary Paper No./Mail Dat 98), 7. ☐ Examiner's Amendn	e	
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DETAILED ACTION

Receipt is acknowledged of the Amendment filed January 18, 2005.

Allowable Subject Matter

Claims 1-20 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: Although Kean teaches partial configuring FPGA and Baxter teaches an FPGA performing PLL, one of ordinary skill in the art would not have been motivated to modify the teaching of Kean and/or Baxter to further includes, among other things, the specific of a programmable identification number associated with the memory interface and set via one or more of the configuration memory cells as required in claim 1, and a system bus register file coupled to the system bus and couplable to the programmable interconnect providing an interface between the memory interface and the system bus as required by claims 11 and 16.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James H. Cho Primary Examiner Art Unit 2819

Date: 2-7-2005